

15 – 17.3.2022

NUREMBERG | GERMANY



CALL FOR PAPERS

The **embedded world Exhibition & Conference** is the premier event worldwide on the topic and continued this path also during the last hybrid and online years. It brings together researchers and developers, industry and academia from all disciplines of embedded system development.

Now in its 20th edition, the **embedded world Exhibition & Conference 2022** (www.embedded-world.eu) will be staged from 15 to 17 March 2022 in Nuremberg. embedded world Conference in collaboration with the European HiPEAC project (HiPEAC, High Performance Embedded Architecture and Compilation, www.hipeac.net) is calling for proposals for a special **HiPEAC** track.

The unique selling propositions of this partnership are:

- in bringing European academia in general and HiPEAC in particular together with a broad industrial audience, thus fostering the exchange of ideas, strengthening the collaboration, and increasing the mutual visibility.
- in contributing and benefiting from the broad system orientation of the embedded world, where special topics are always regarded from the viewpoint of a complete system, and where general approaches are dissected from concrete specialists.

The special topics of the HiPEAC session shall include:

- Processor, memory, and storage systems architecture
- Parallel, multi-core and heterogeneous systems
- Interconnection networks
- Architectural support for programming productivity
- Power, performance and implementation efficient designs
- Reliability and real-time support in processors, compilers and run-time systems
- Application-specific processors, accelerators and reconfigurable processors
- Architecture and programming environments for GPU-based computing
- Simulation and methodology
- Architectural and run-time support for programming languages
- Programming models, frameworks and environments for exploiting parallelism
- Compiler techniques
- Feedback-directed optimization
- Program characterization and analysis techniques
- Dynamic compilation, adaptive execution, and continuous profiling/optimization
- Binary translation/optimization
- Code size/memory footprint optimizations

Please submit your proposal at www.embedded-world.eu/call-for-papers until **29th Sep 2021**, selecting the topic **“11.4: High Performance Embedded Architectures”**.

Authors will be notified by November 22, 2021. Final Paper (ISBN) and Presentation shall be submitted until February 18, 2022. The conference proceedings will be published soon after the event.